

FIG.1

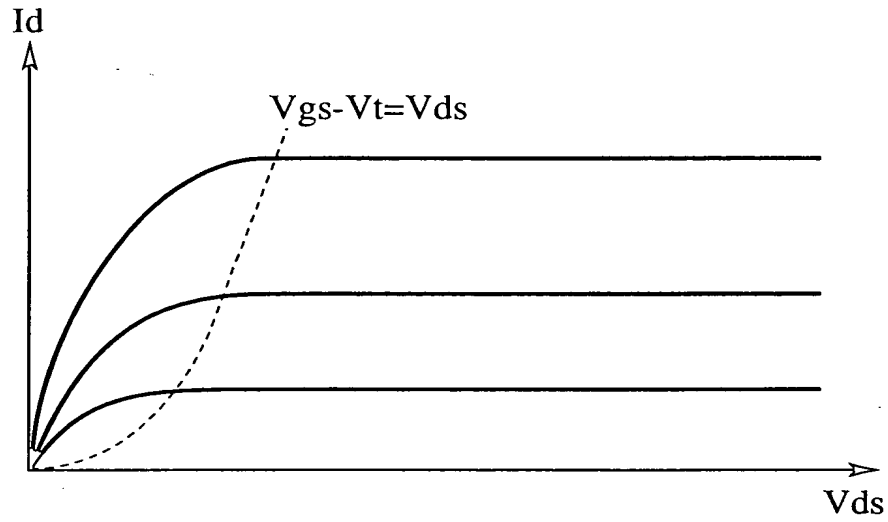
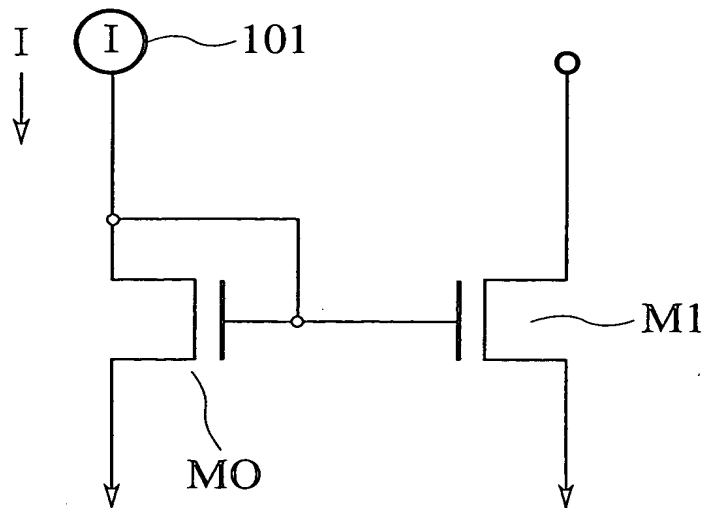


FIG.2



[illegible]

The circuit diagram shows a differential amplifier circuit 114. It includes a current source 115, a differential pair of transistors 111 and 112, a resistor network with resistors R_1 , R_2 , R_3 , R_4 , and an operational amplifier 141. The output of the differential pair is connected to the non-inverting input of the op-amp. The op-amp output is connected to the gate of transistor 113. The circuit is labeled with various nodes and components.

FIG.5

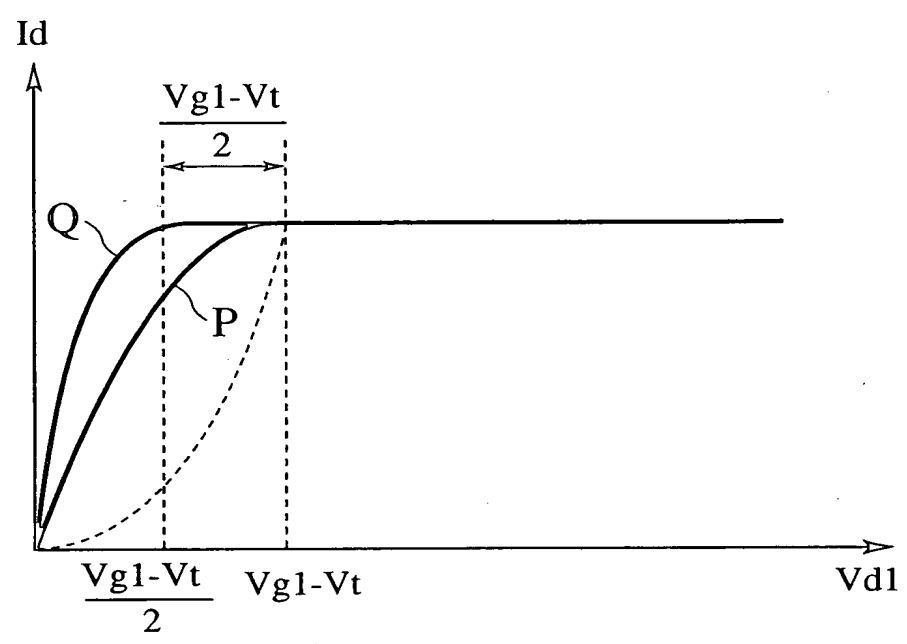
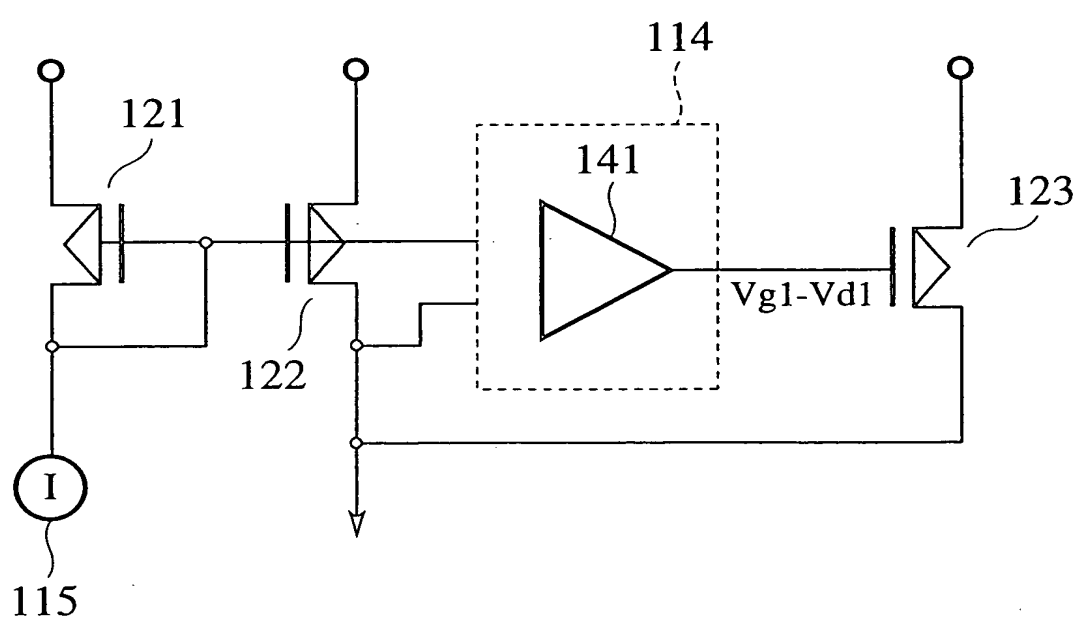


FIG.6



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FIG. 7

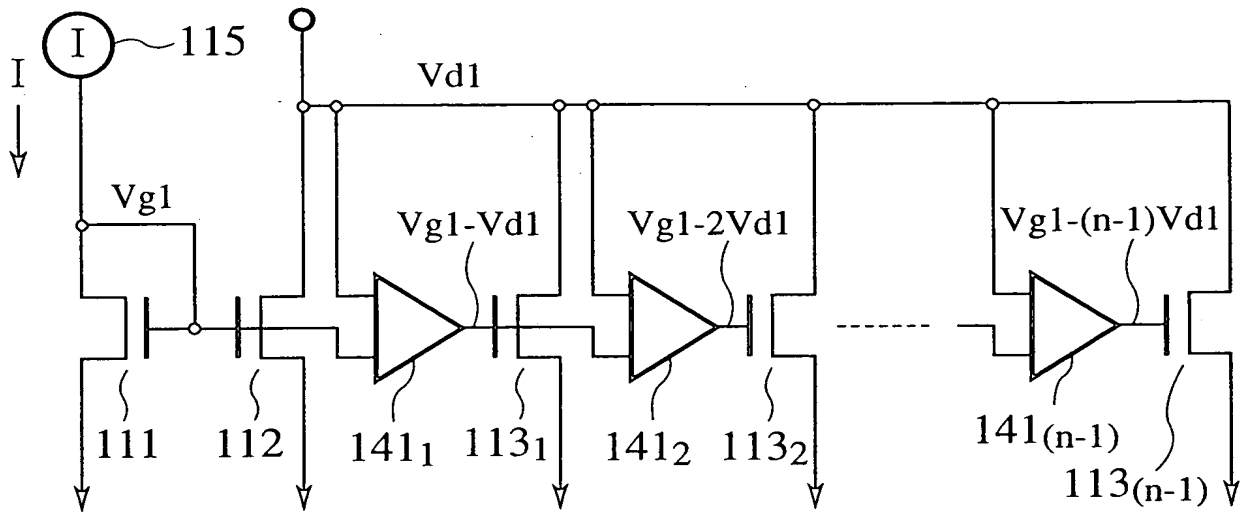
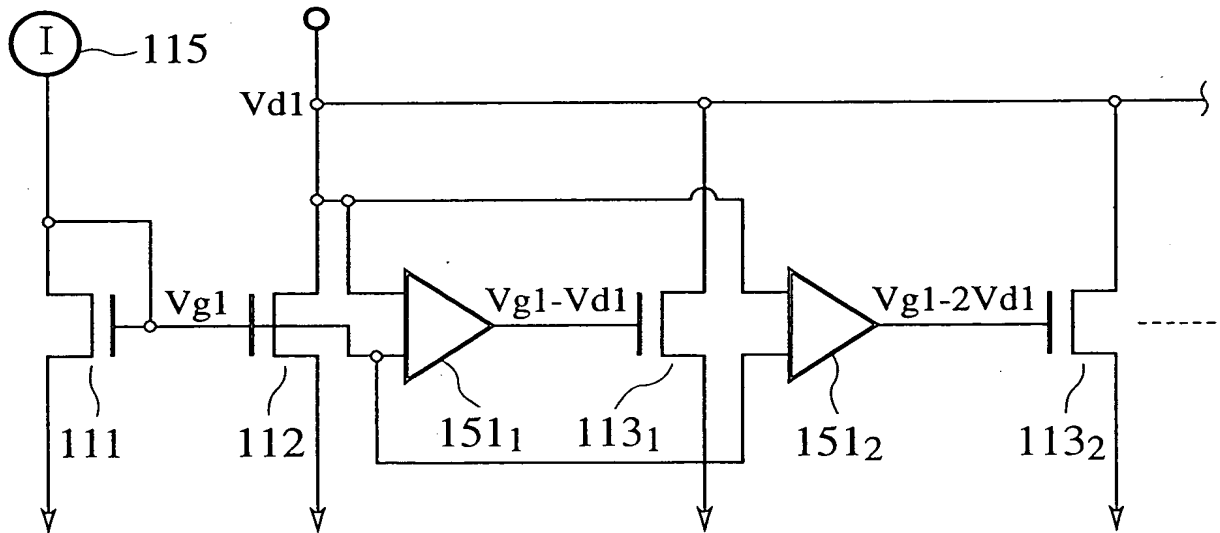


FIG. 8



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FIG. 9

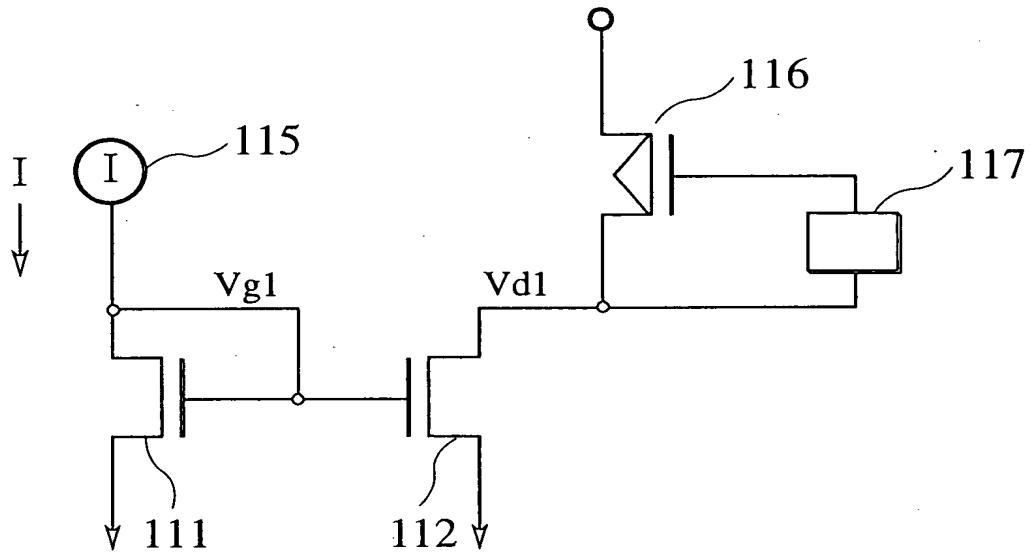
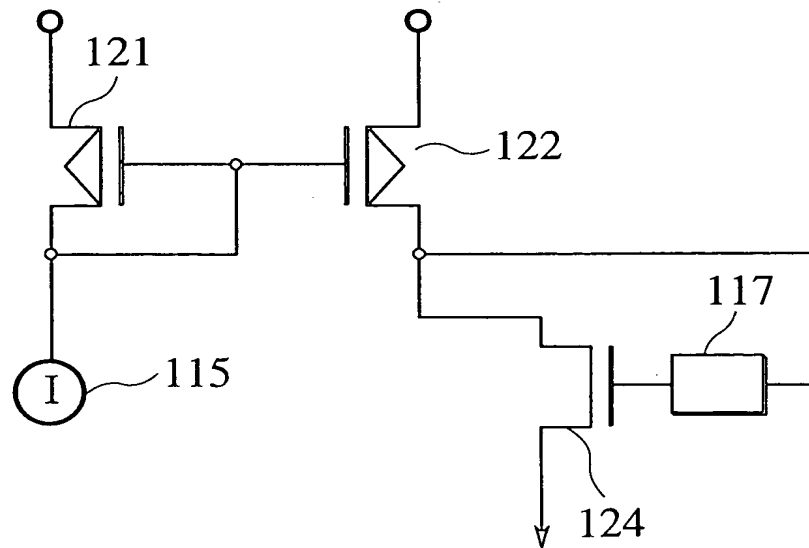


FIG. 10



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FIG. 11

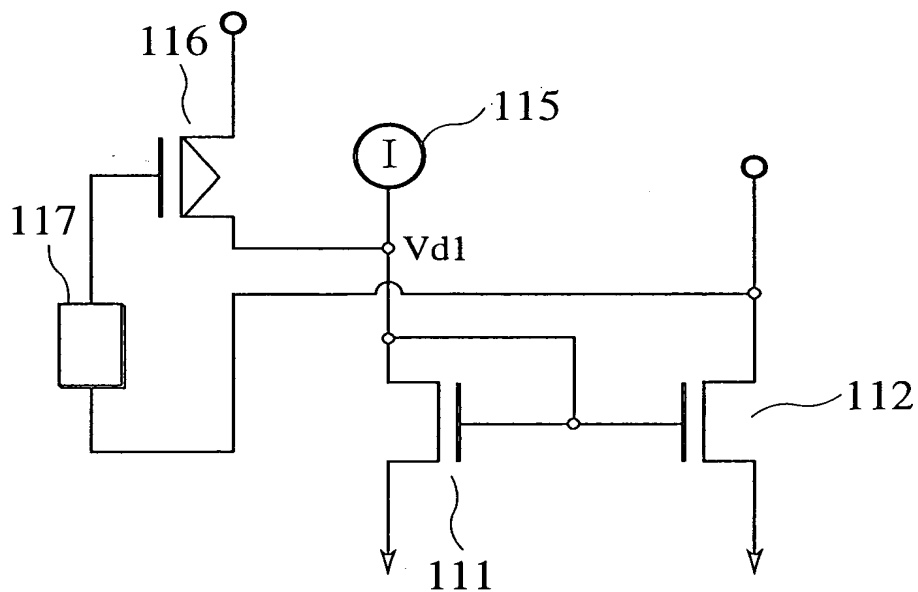
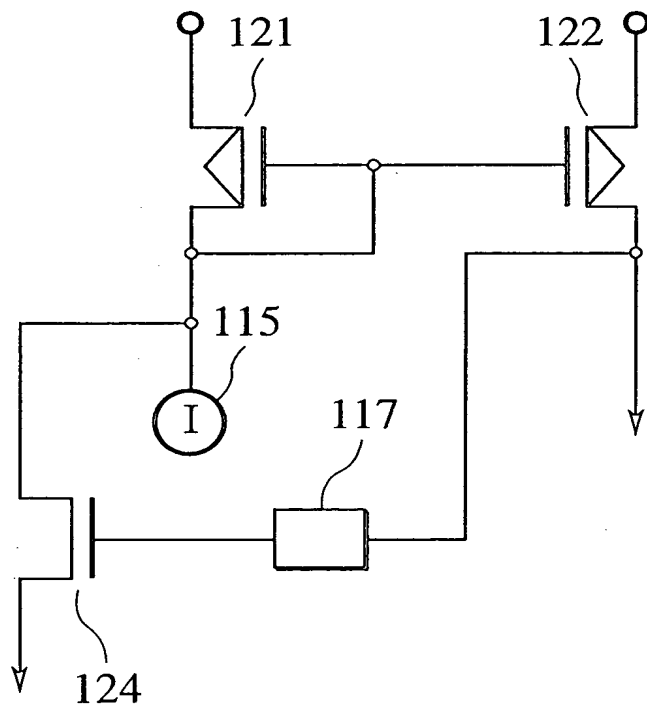


FIG. 12



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FIG. 13

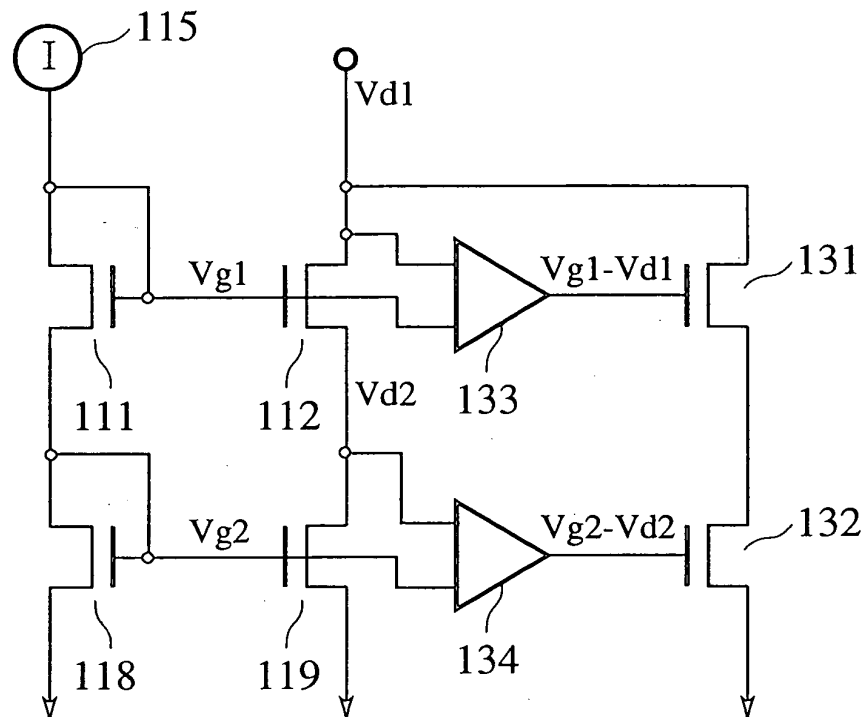
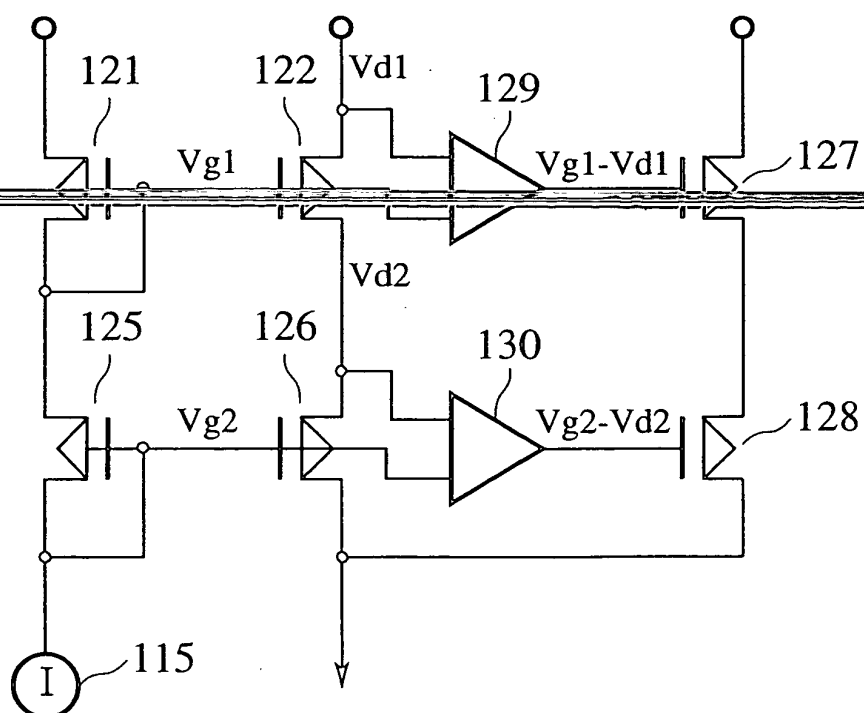


FIG. 14



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FIG.15

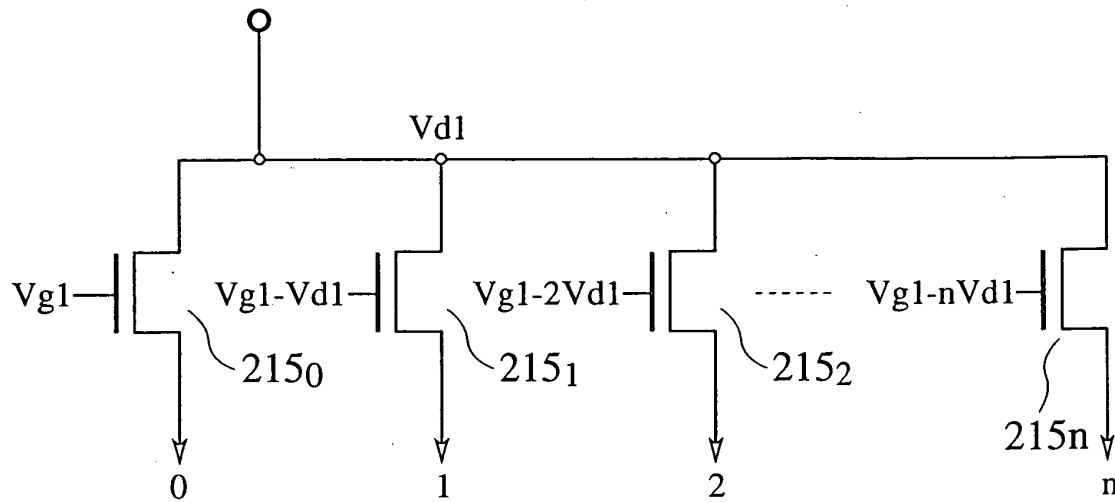
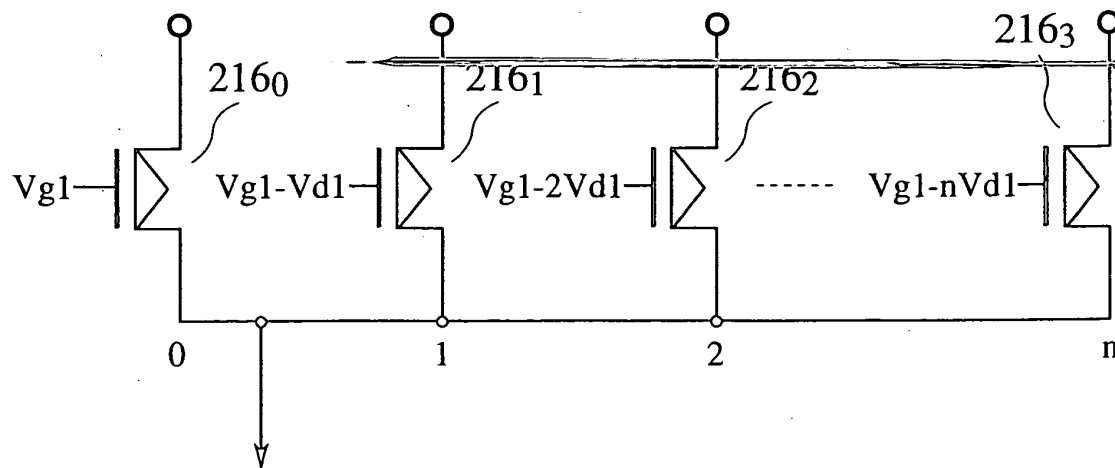


FIG.16





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FIG. 17

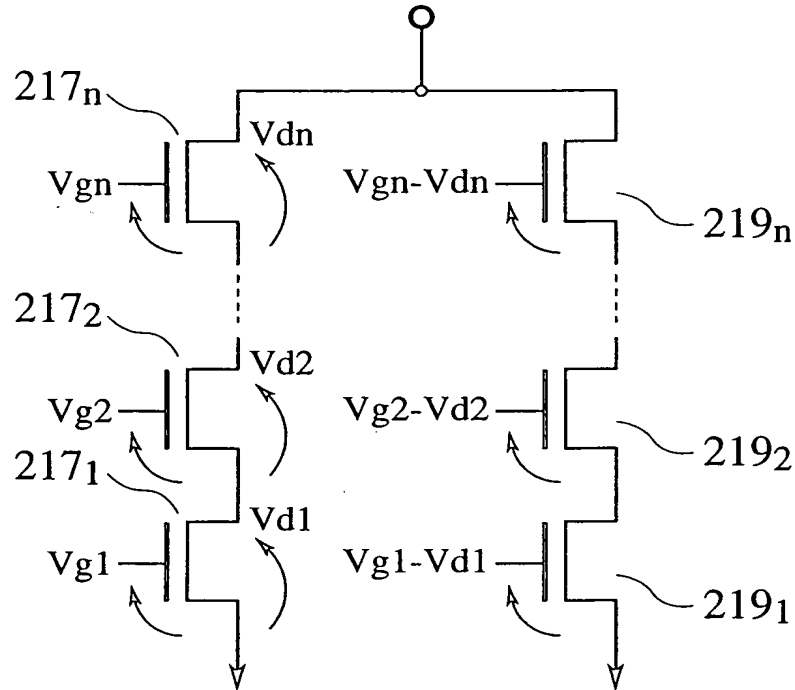
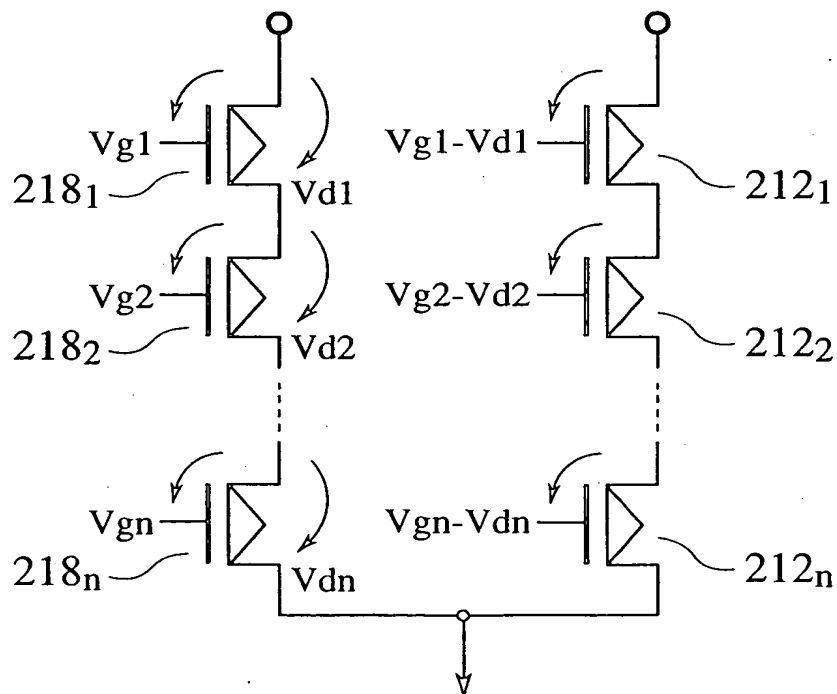


FIG. 18



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